

Fig.1

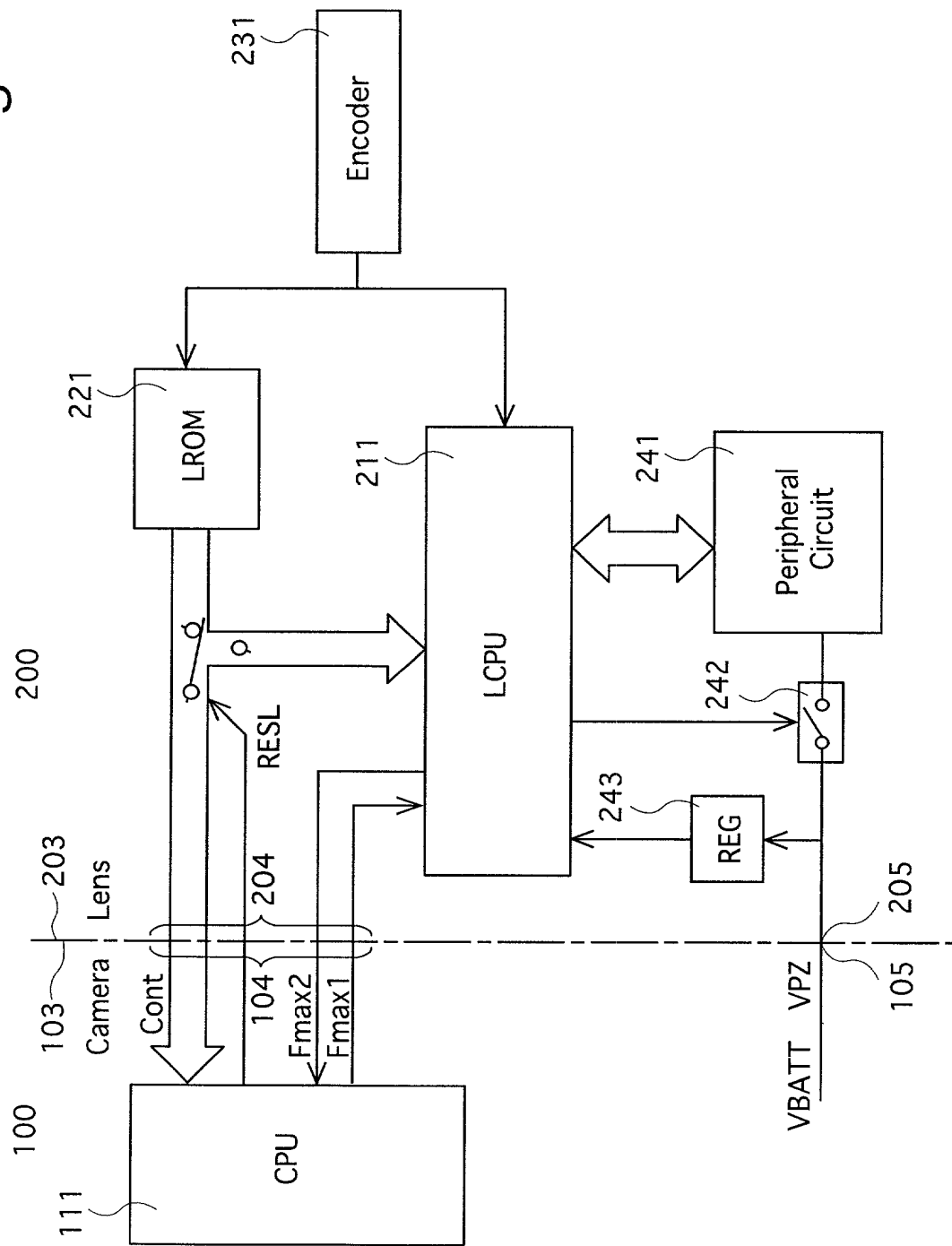
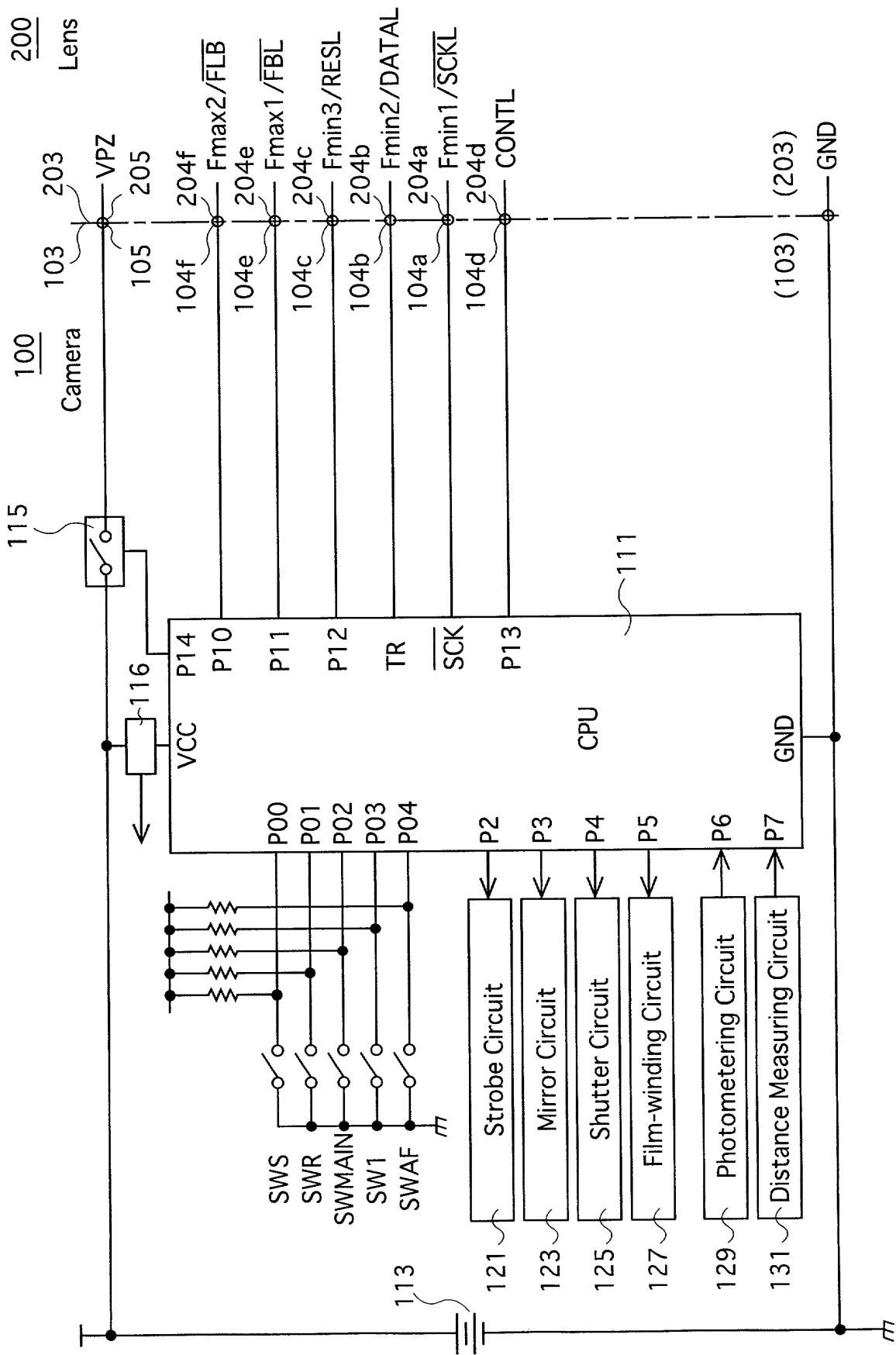


Fig.2



# Fig.3

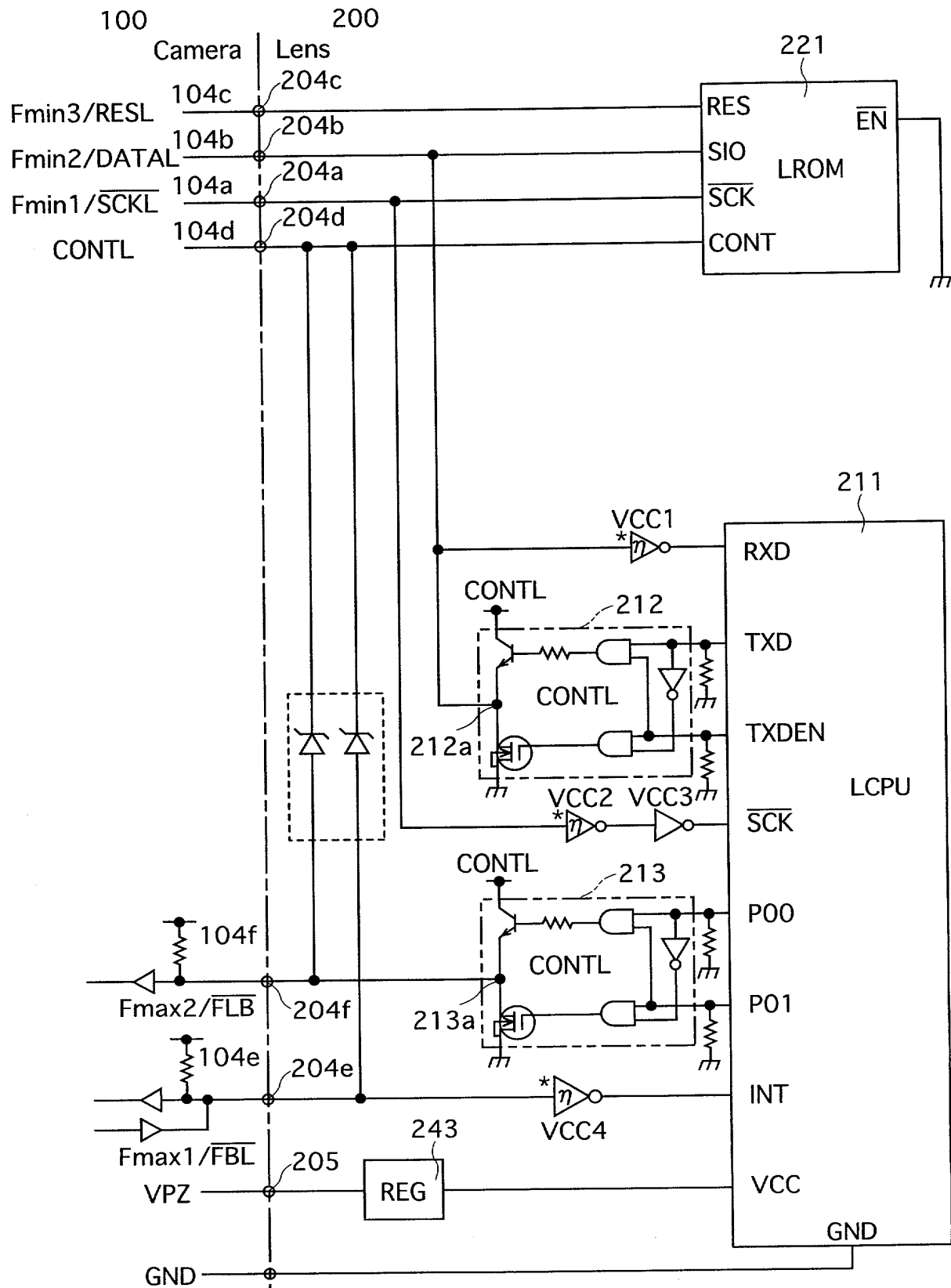


Fig. 4

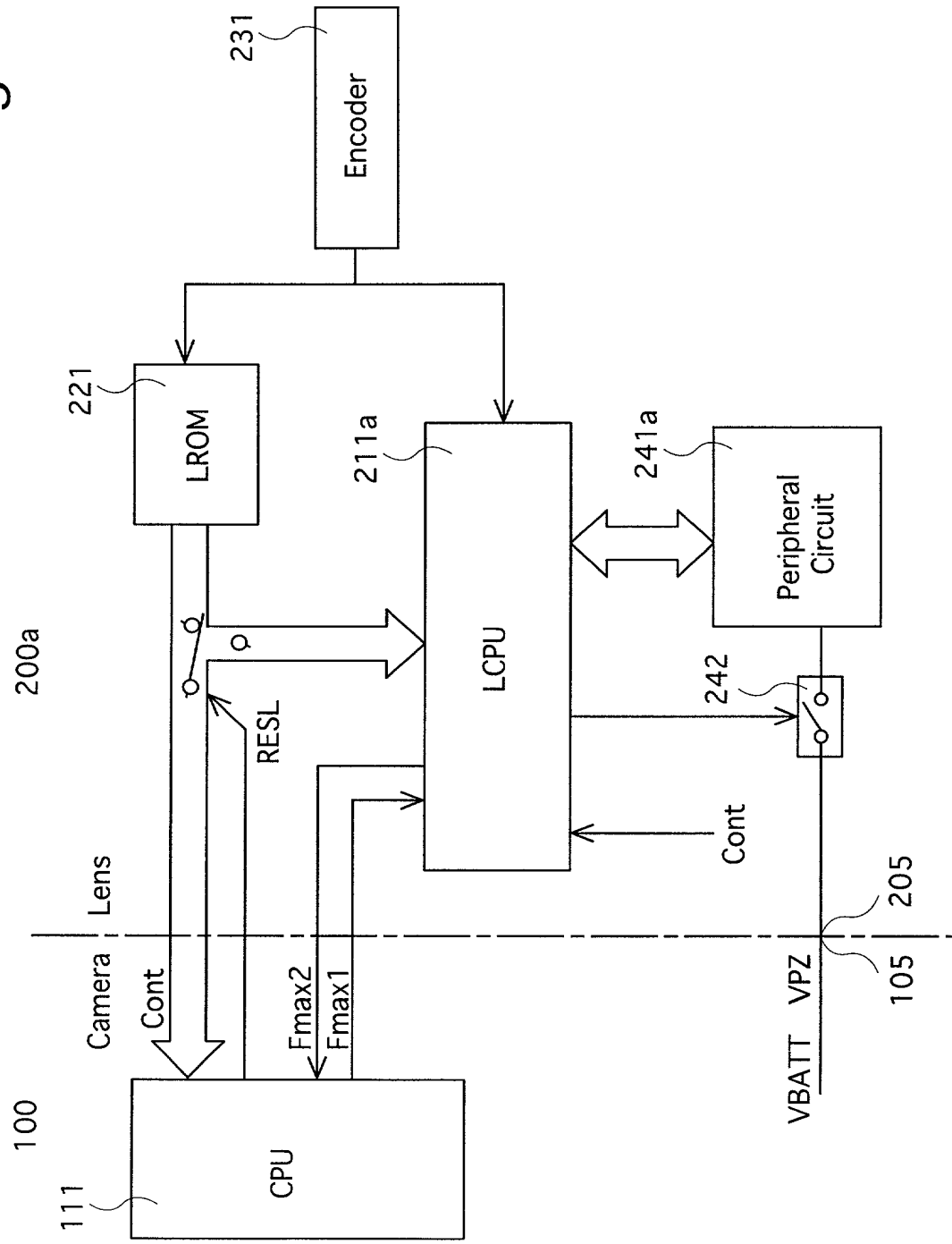


Fig. 5

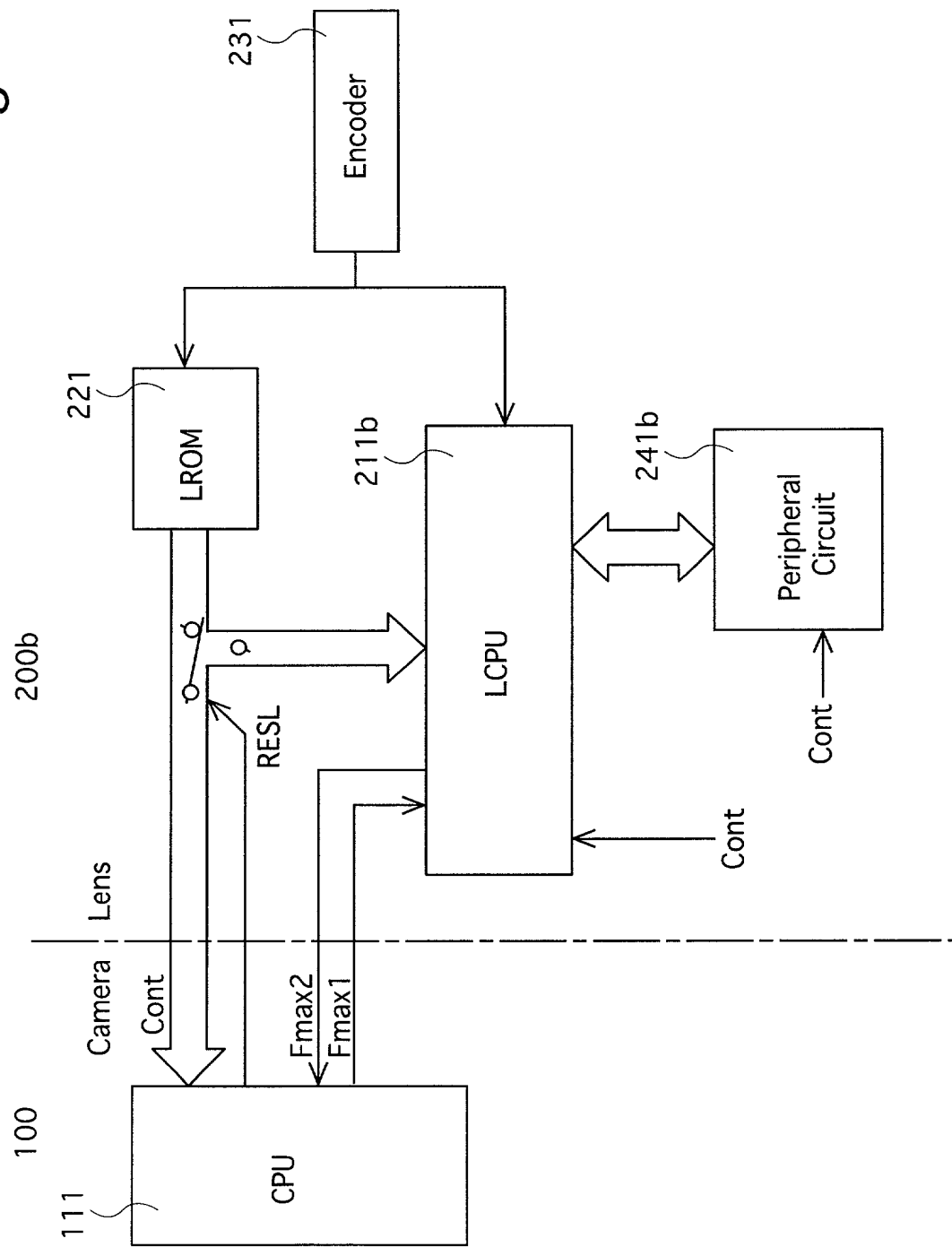


Fig. 6A

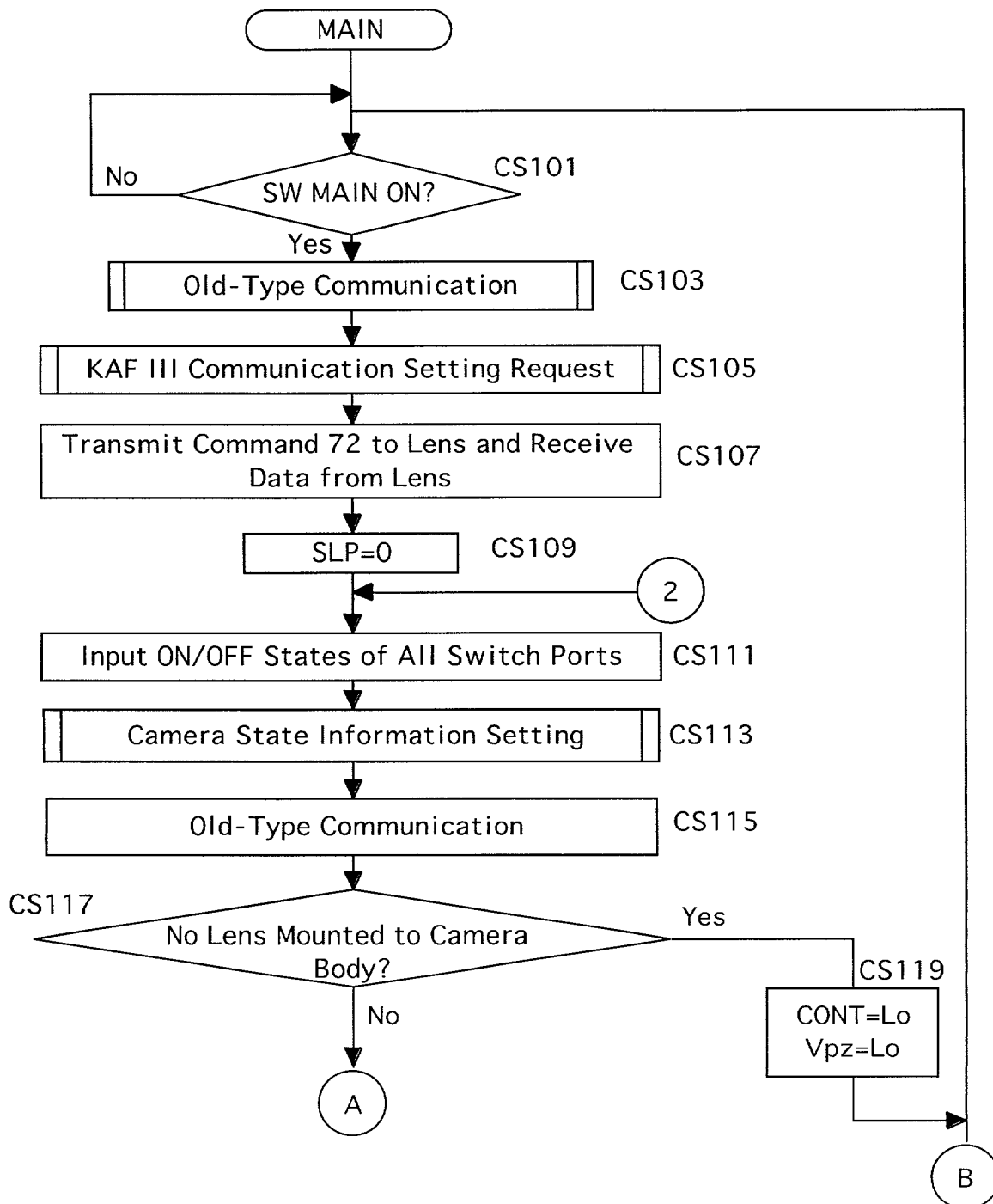


Fig. 6B

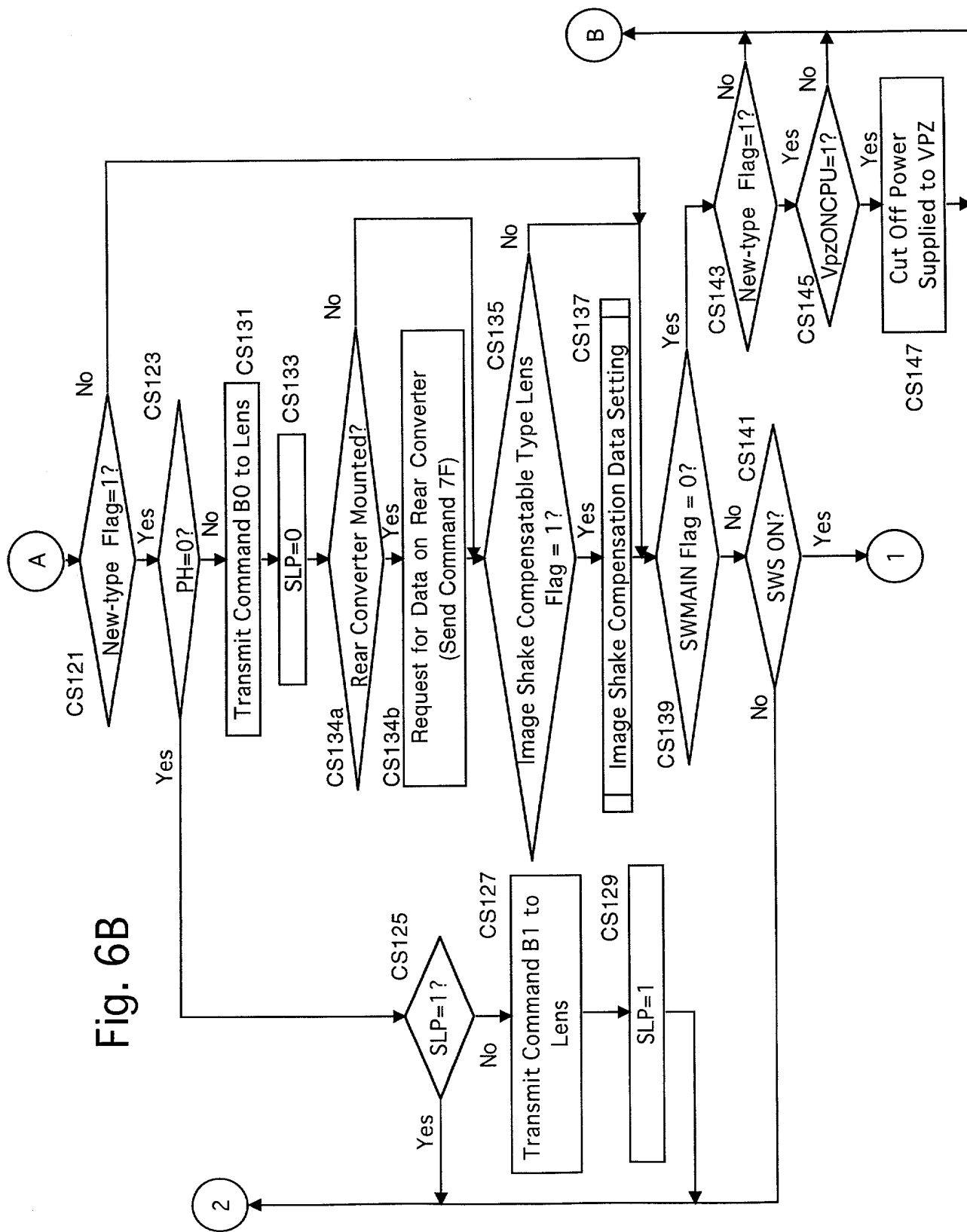
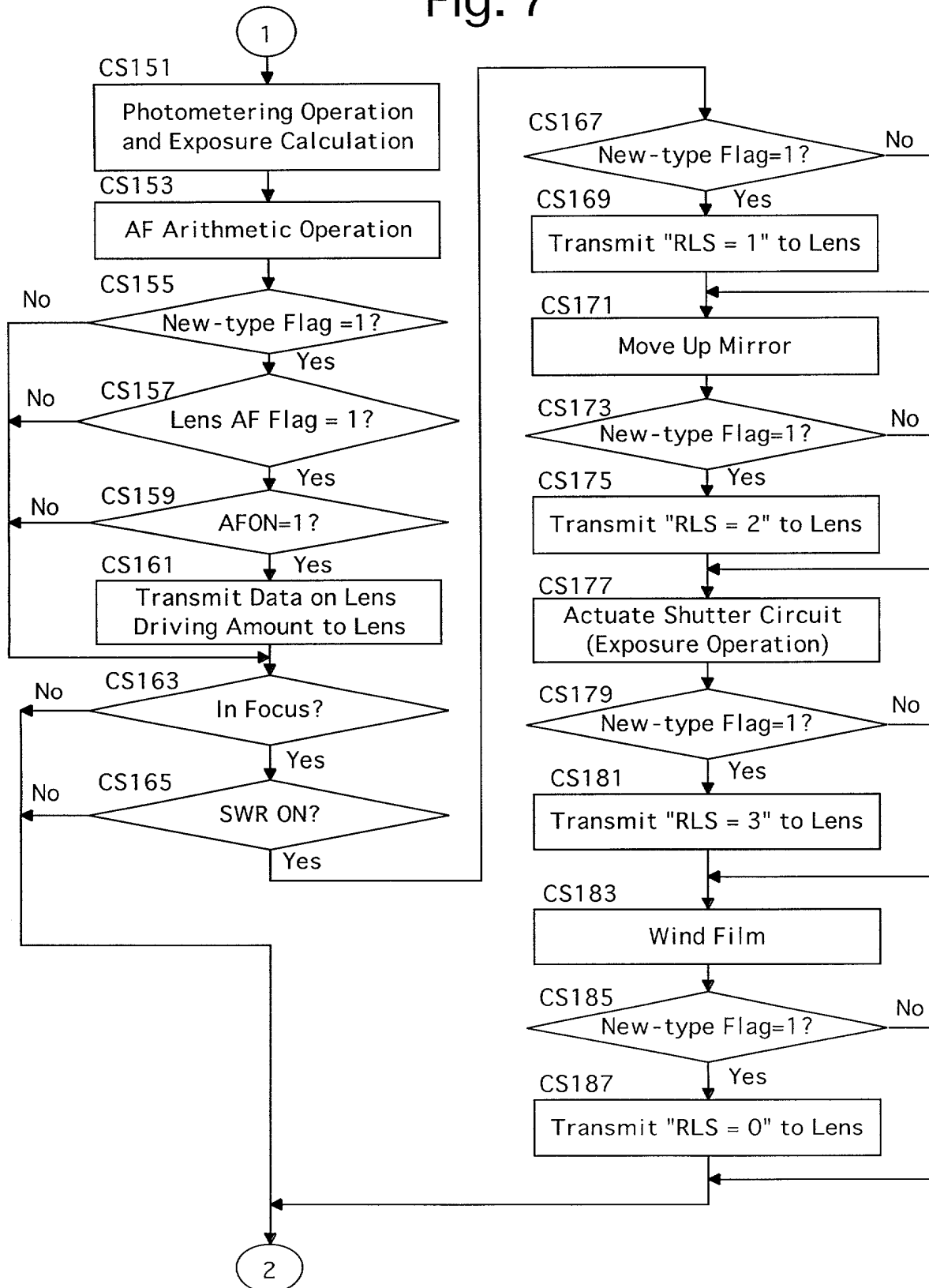
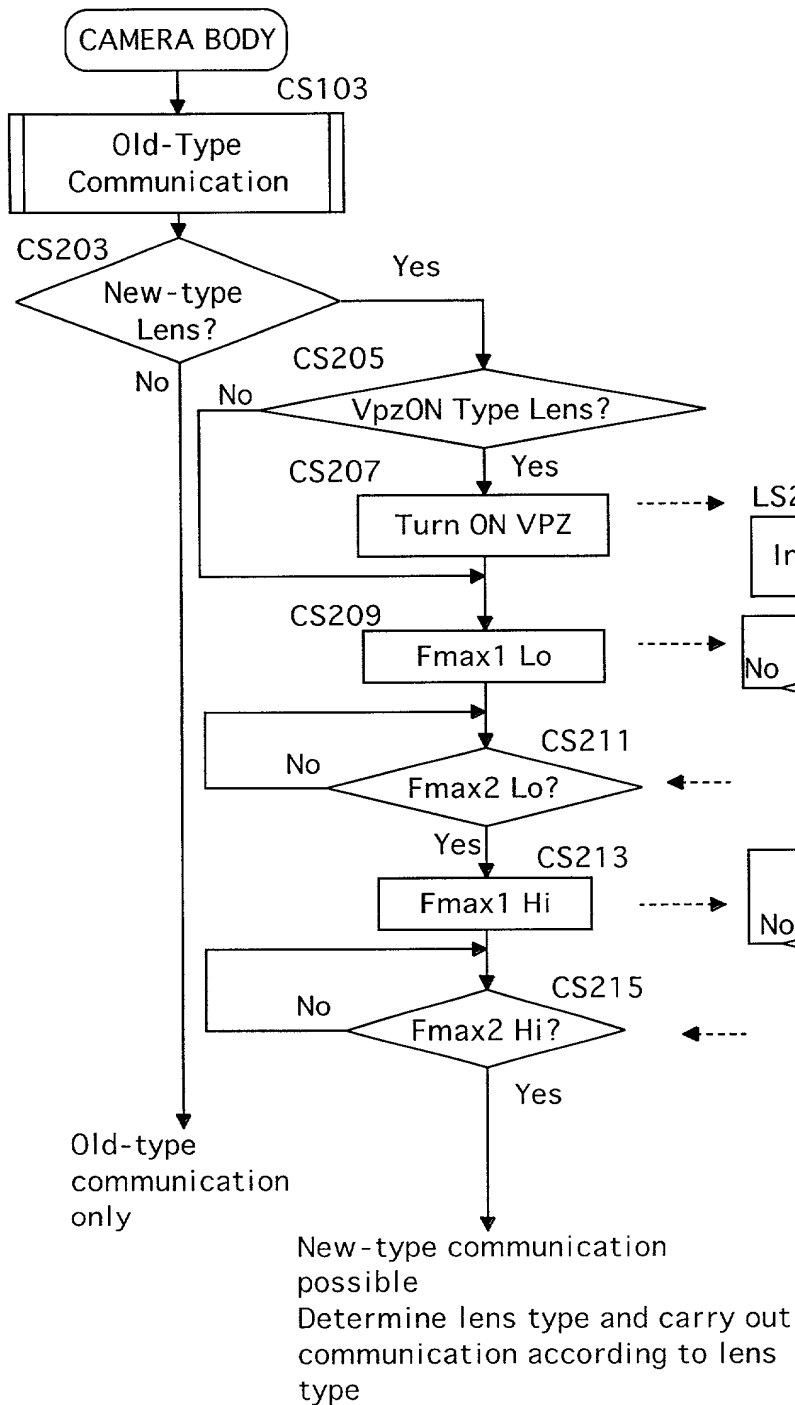


Fig. 7





# Fig. 8A



# Fig.8B

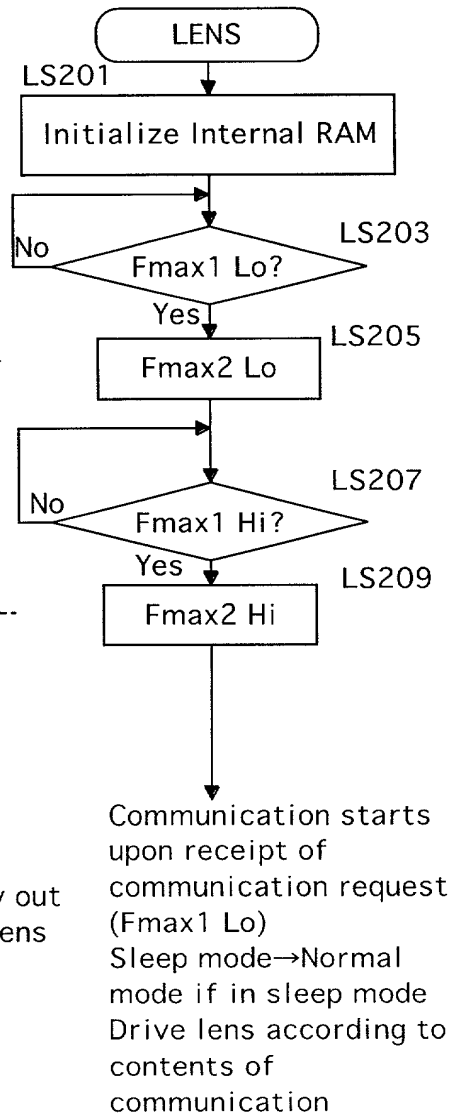


Fig.9

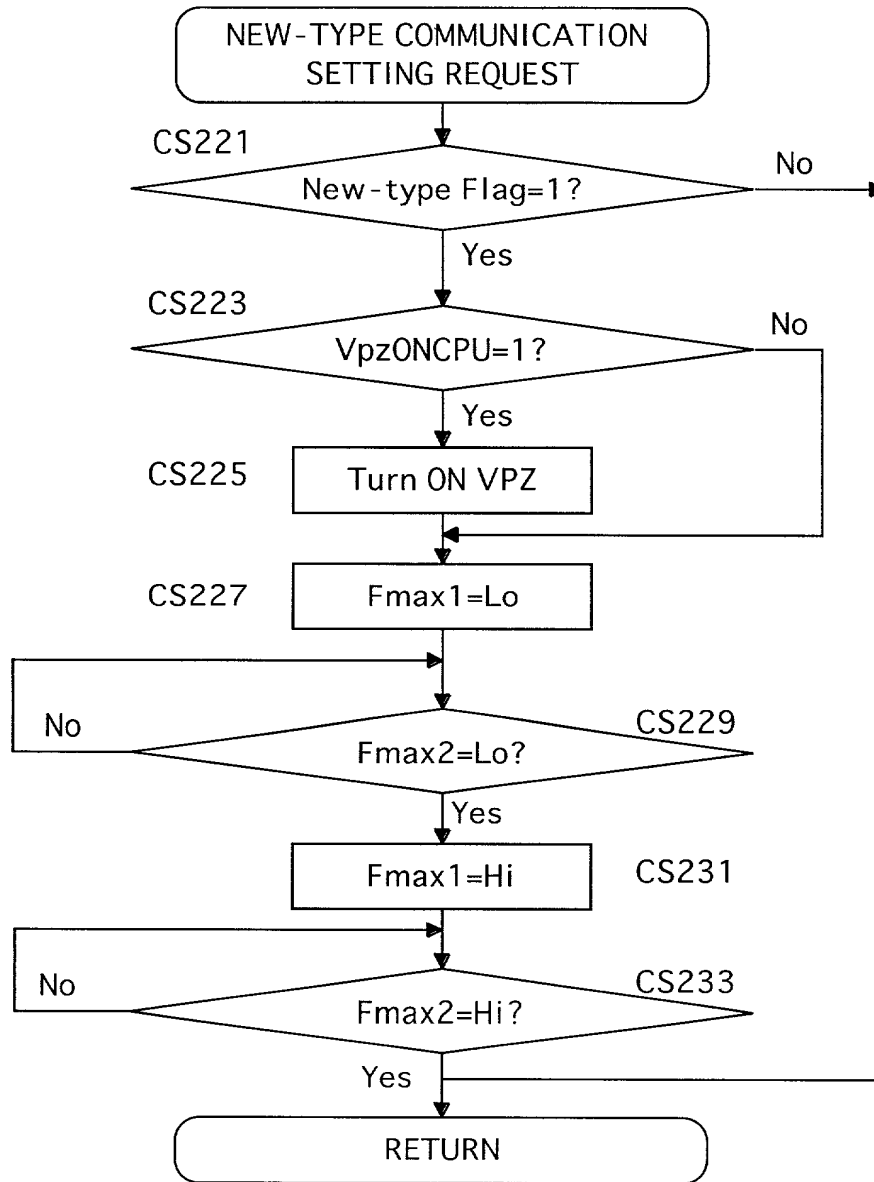


Fig.10

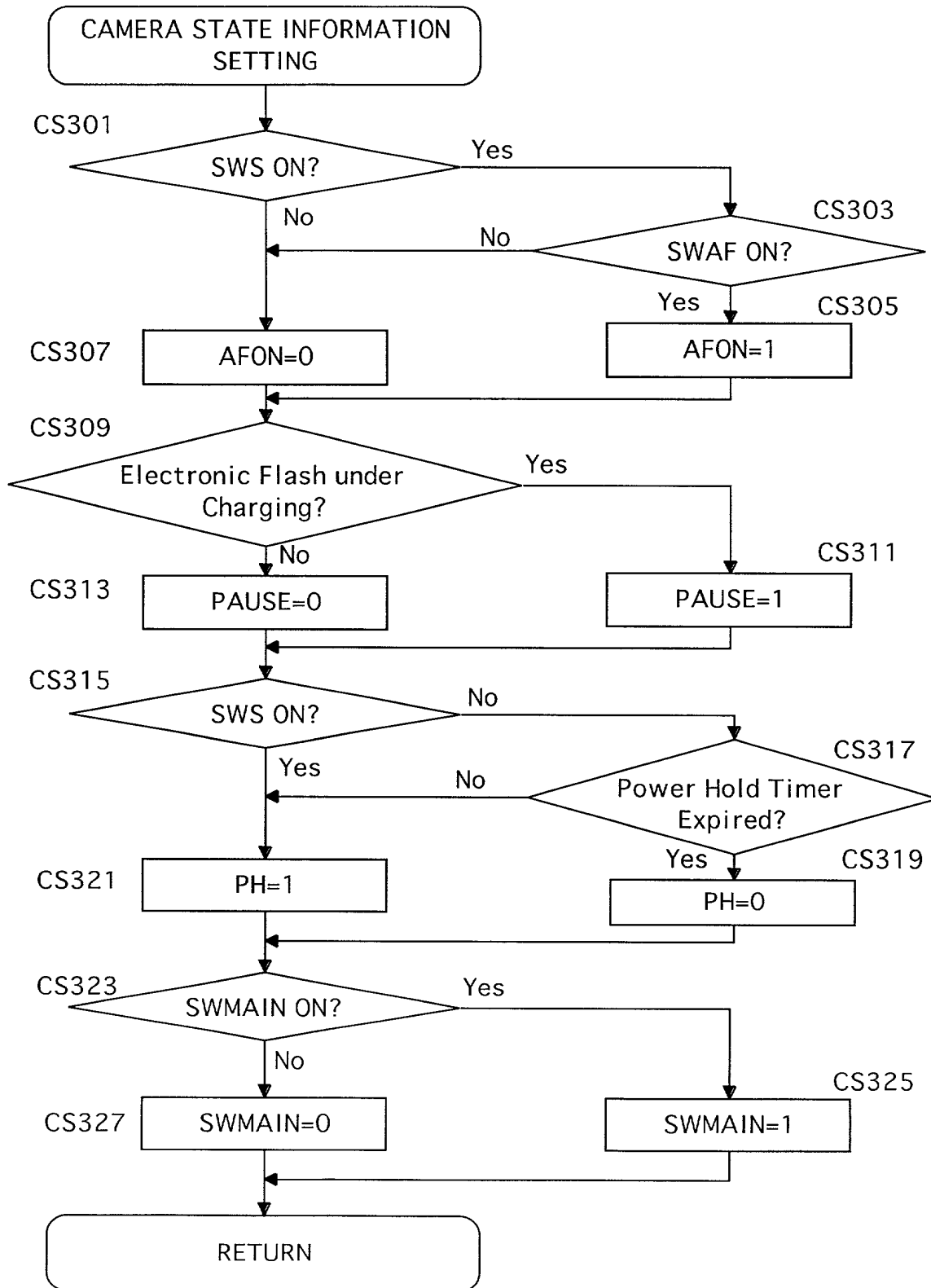


Fig.11

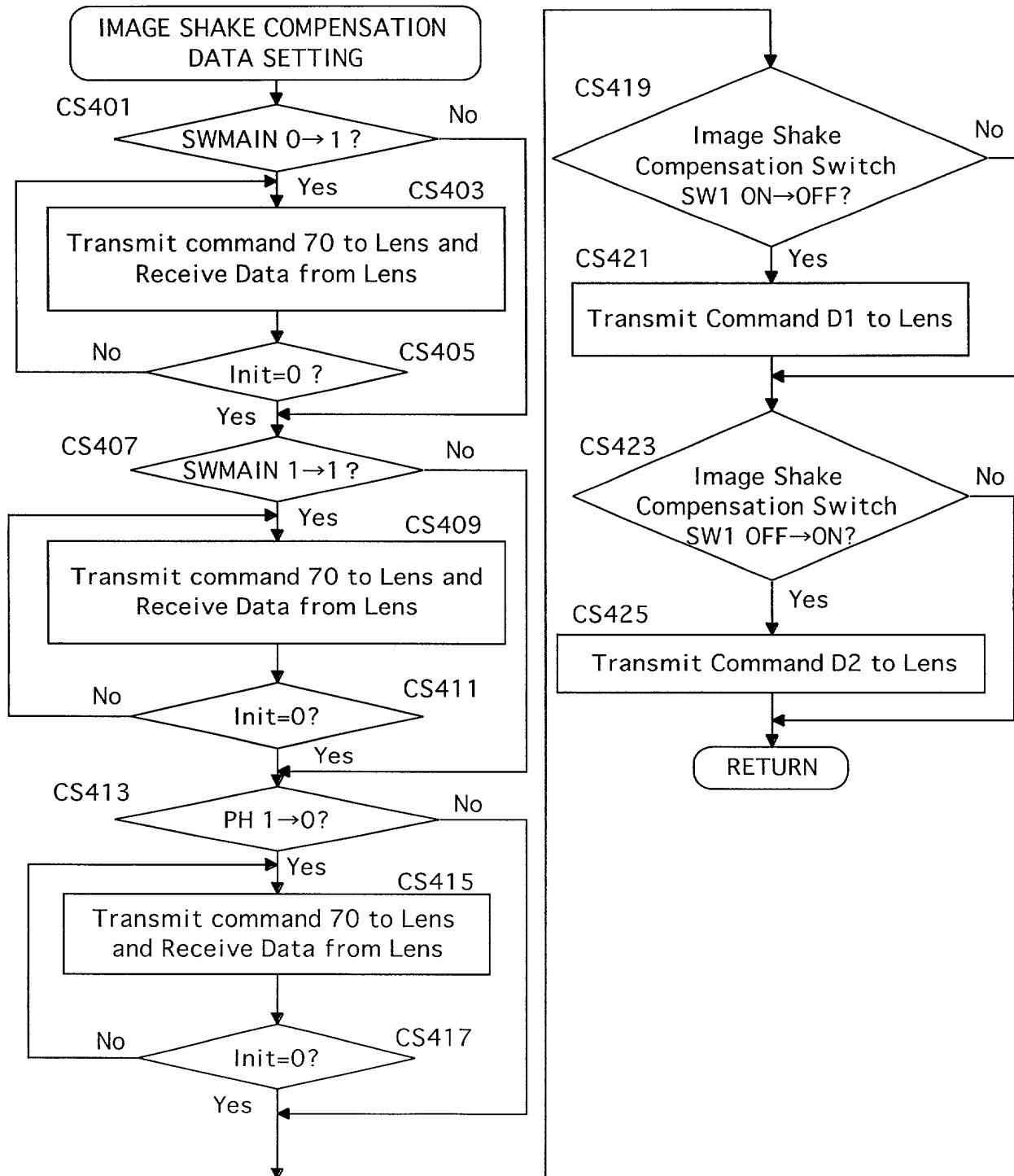
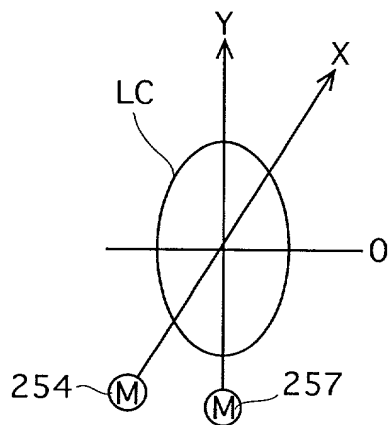




Fig. 12B



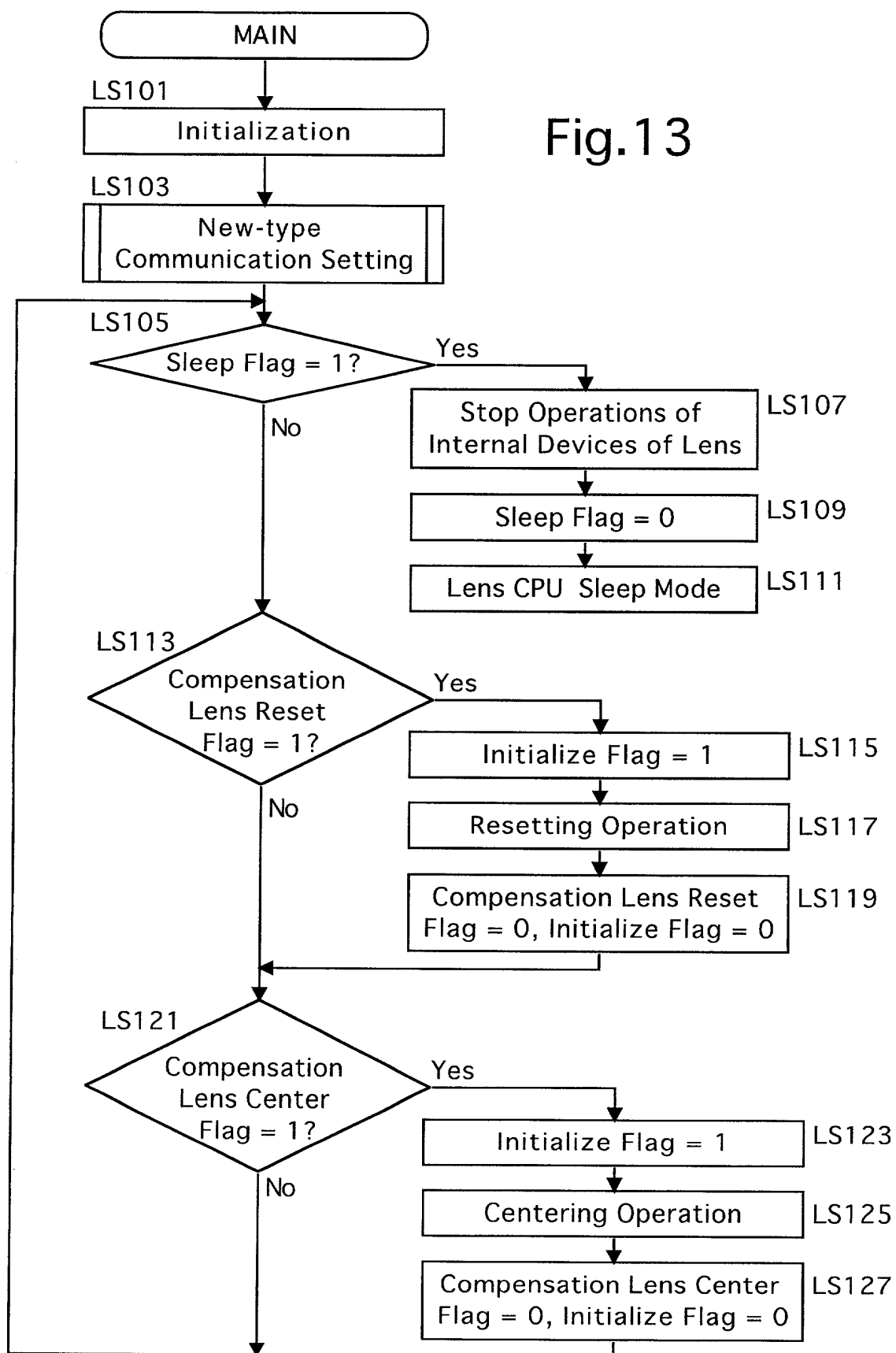
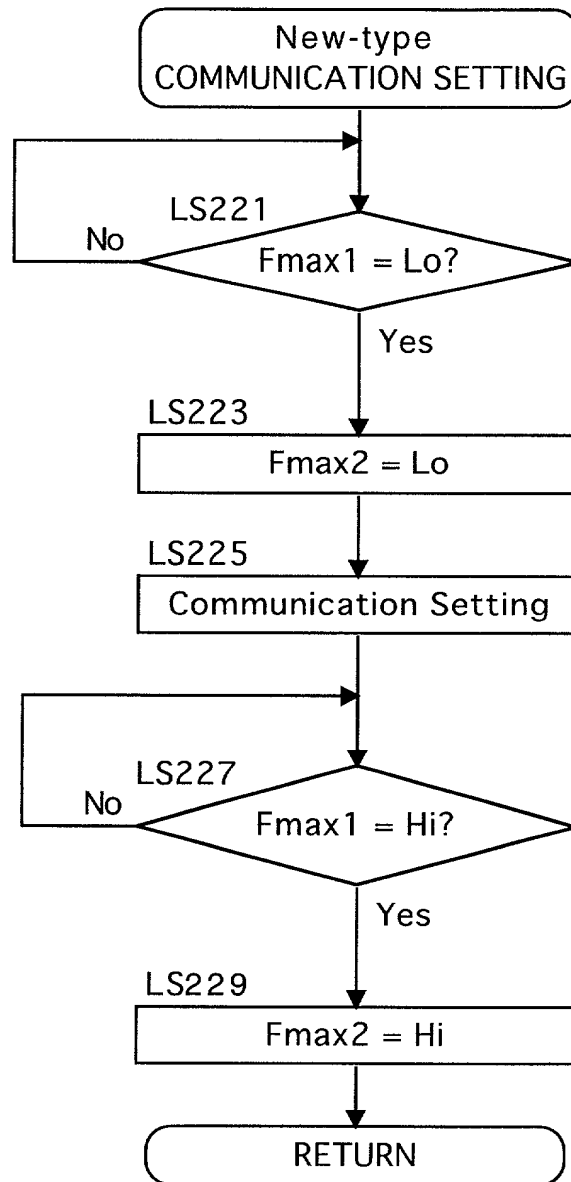
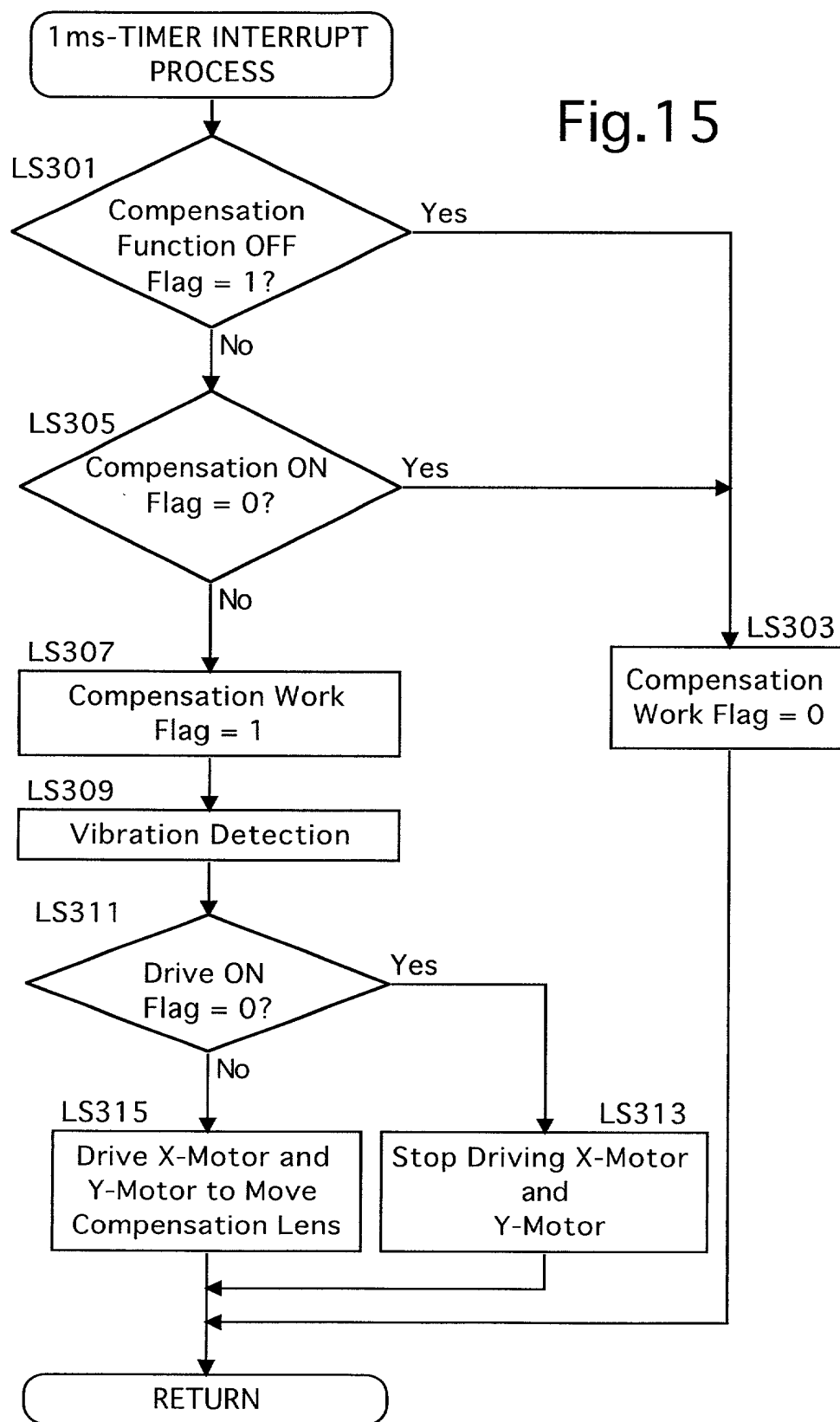


Fig.14



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# INVERSE-INT INTERRUPT PROCESS

Fig.16

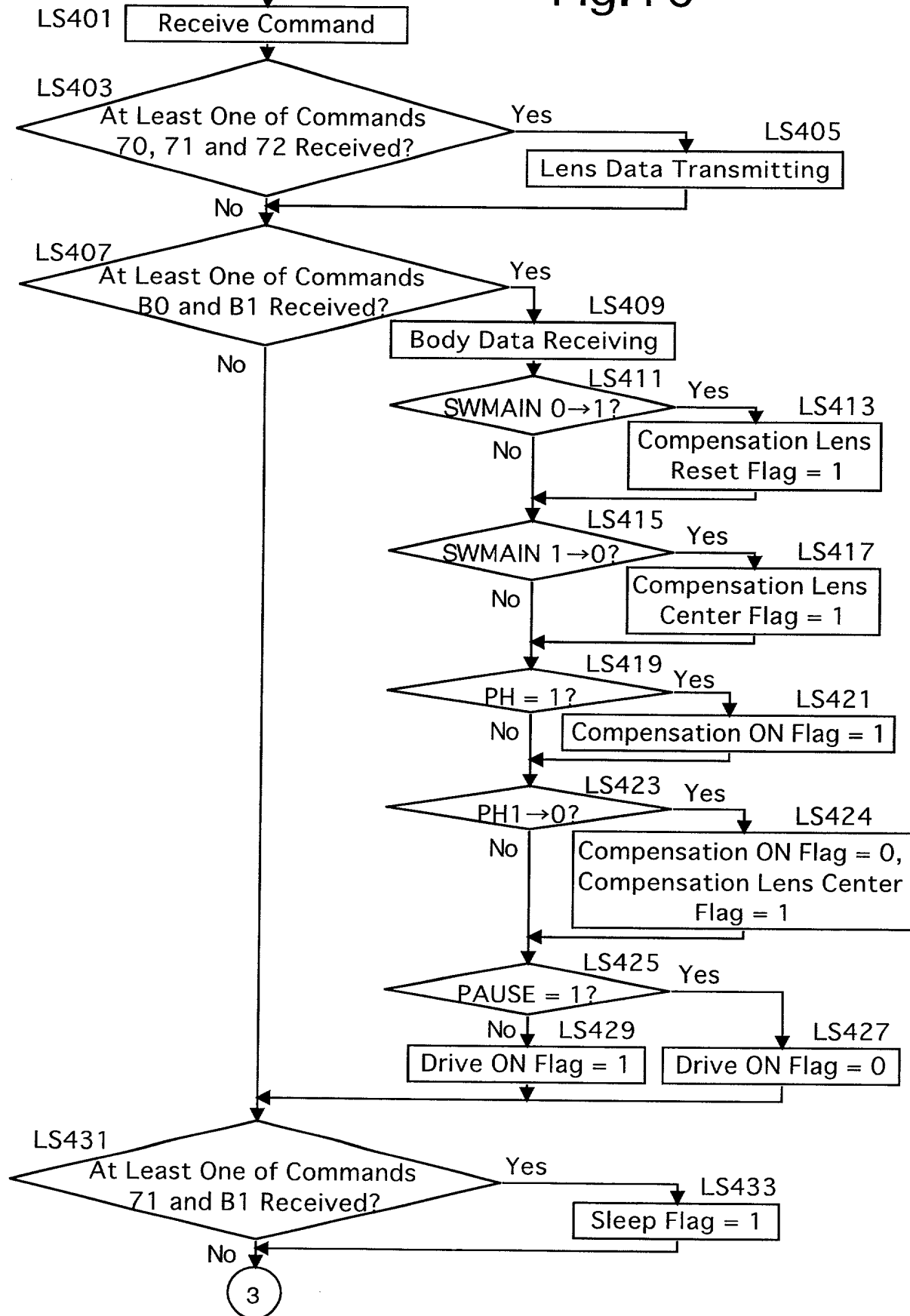


Fig.17



Fig. 18

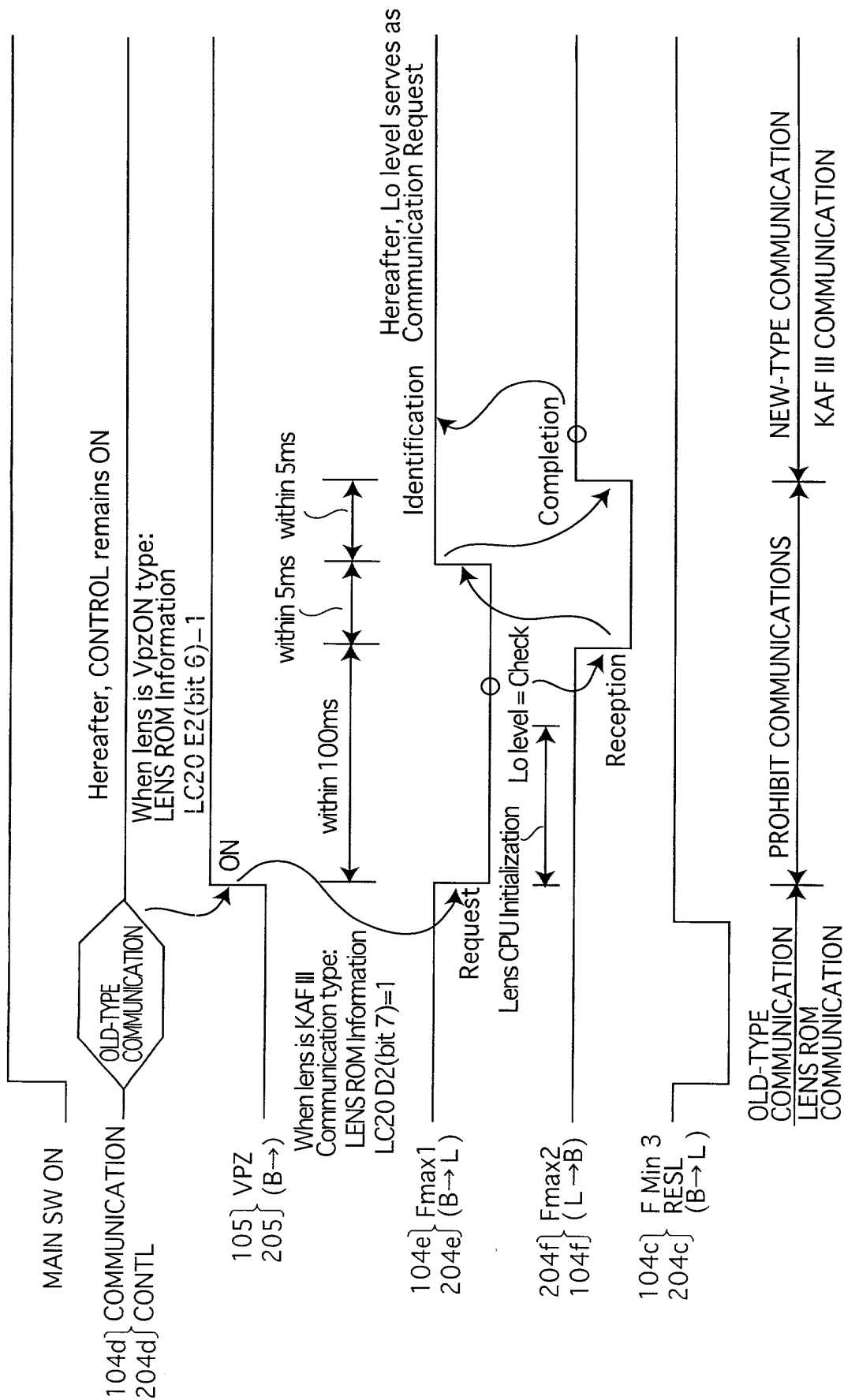


Fig. 19A

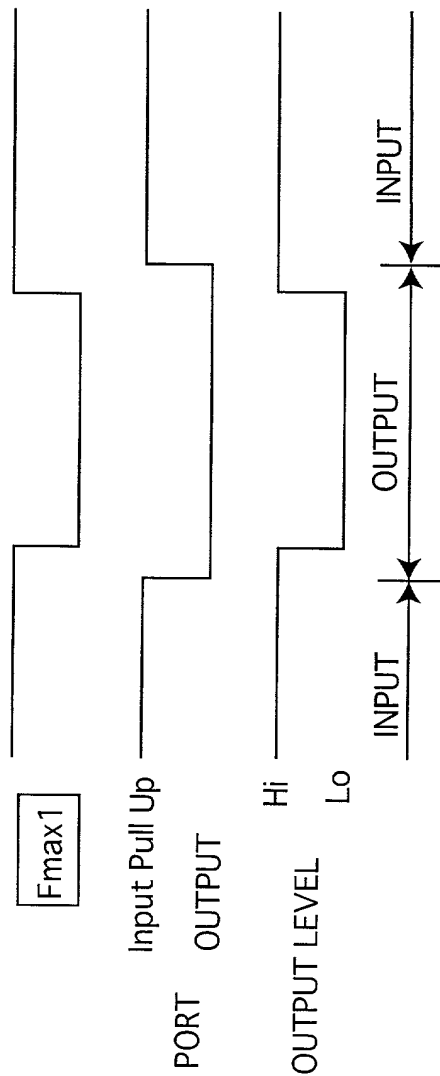


Fig. 19B

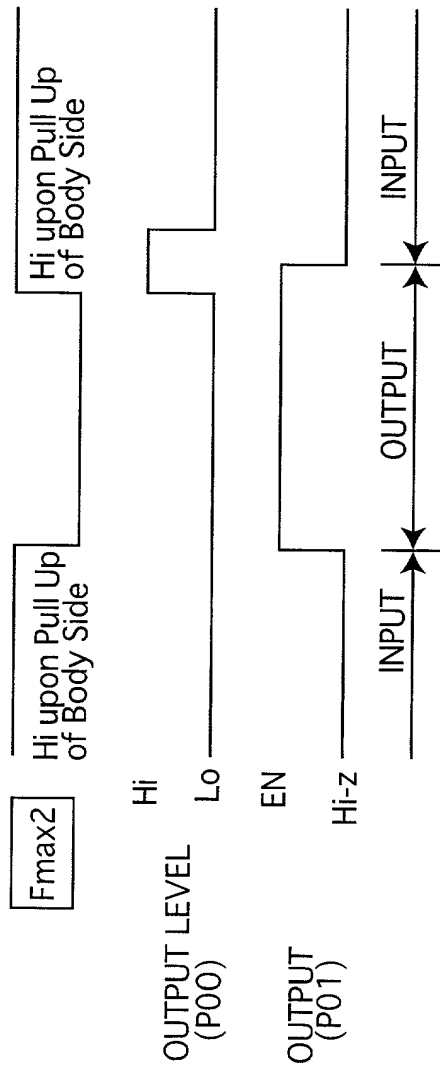


Fig. 20

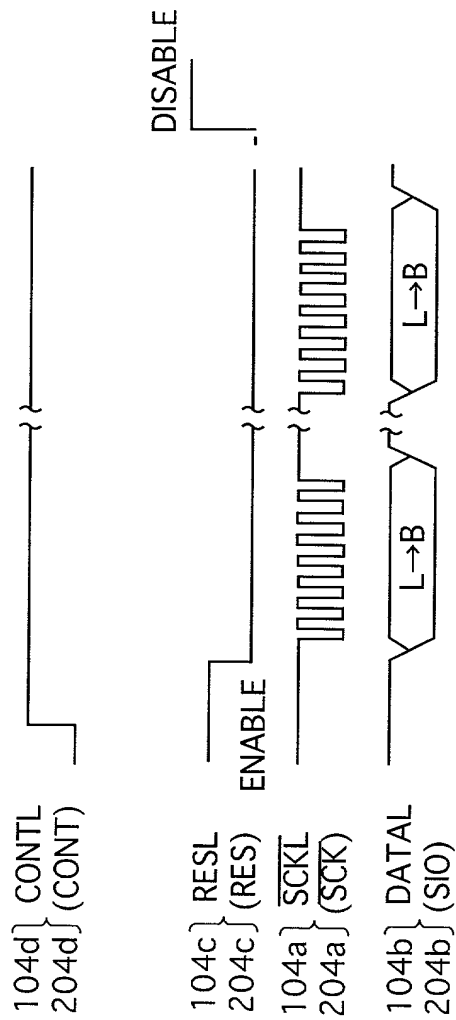


Fig.21A

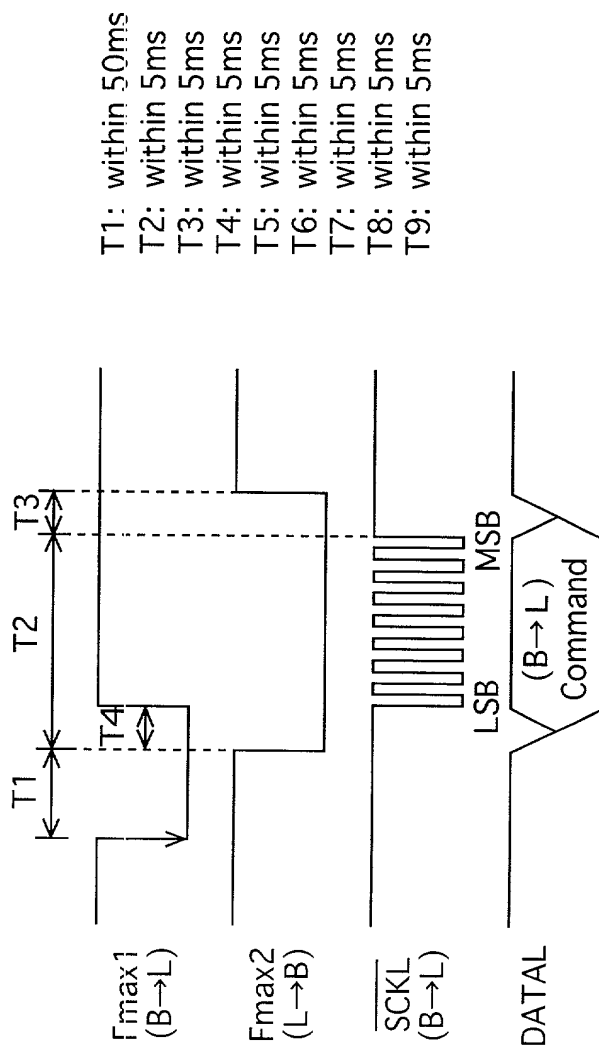
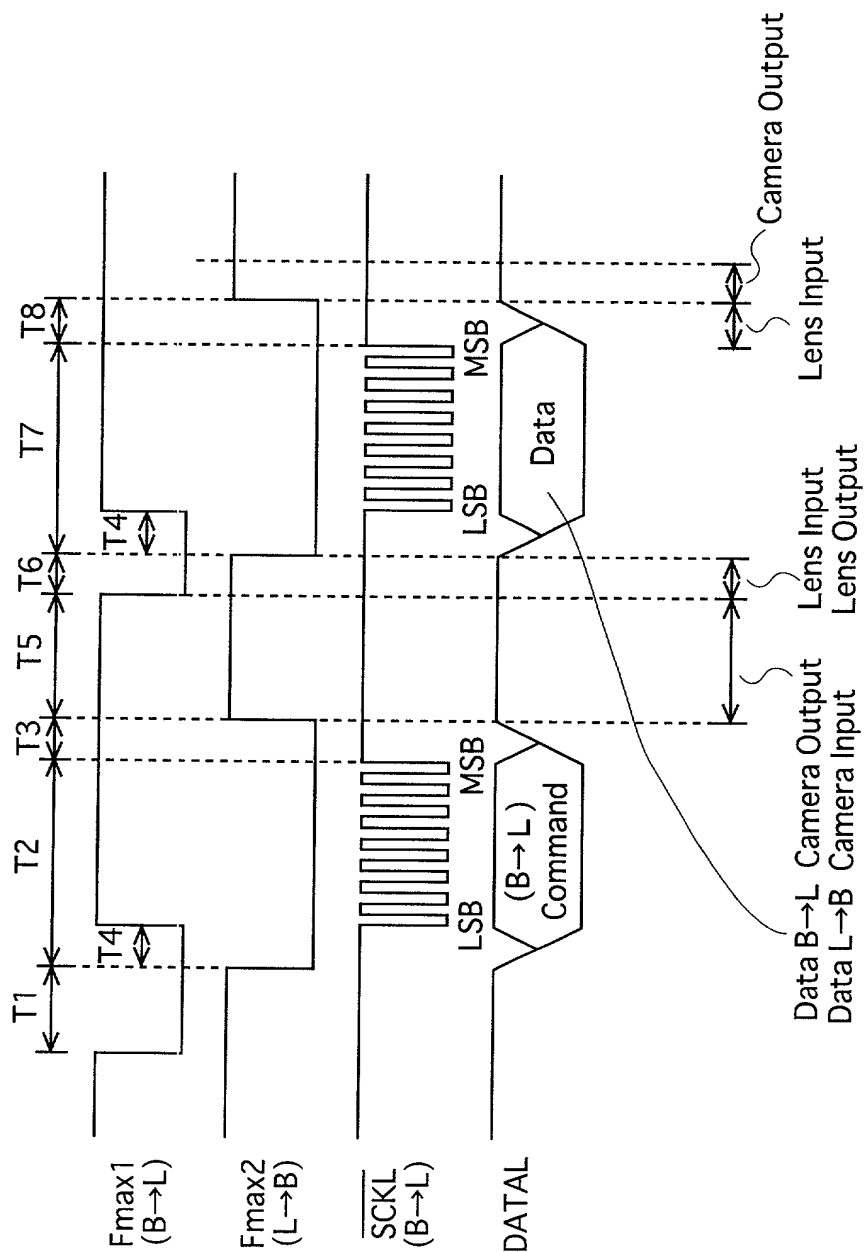


Fig.21B





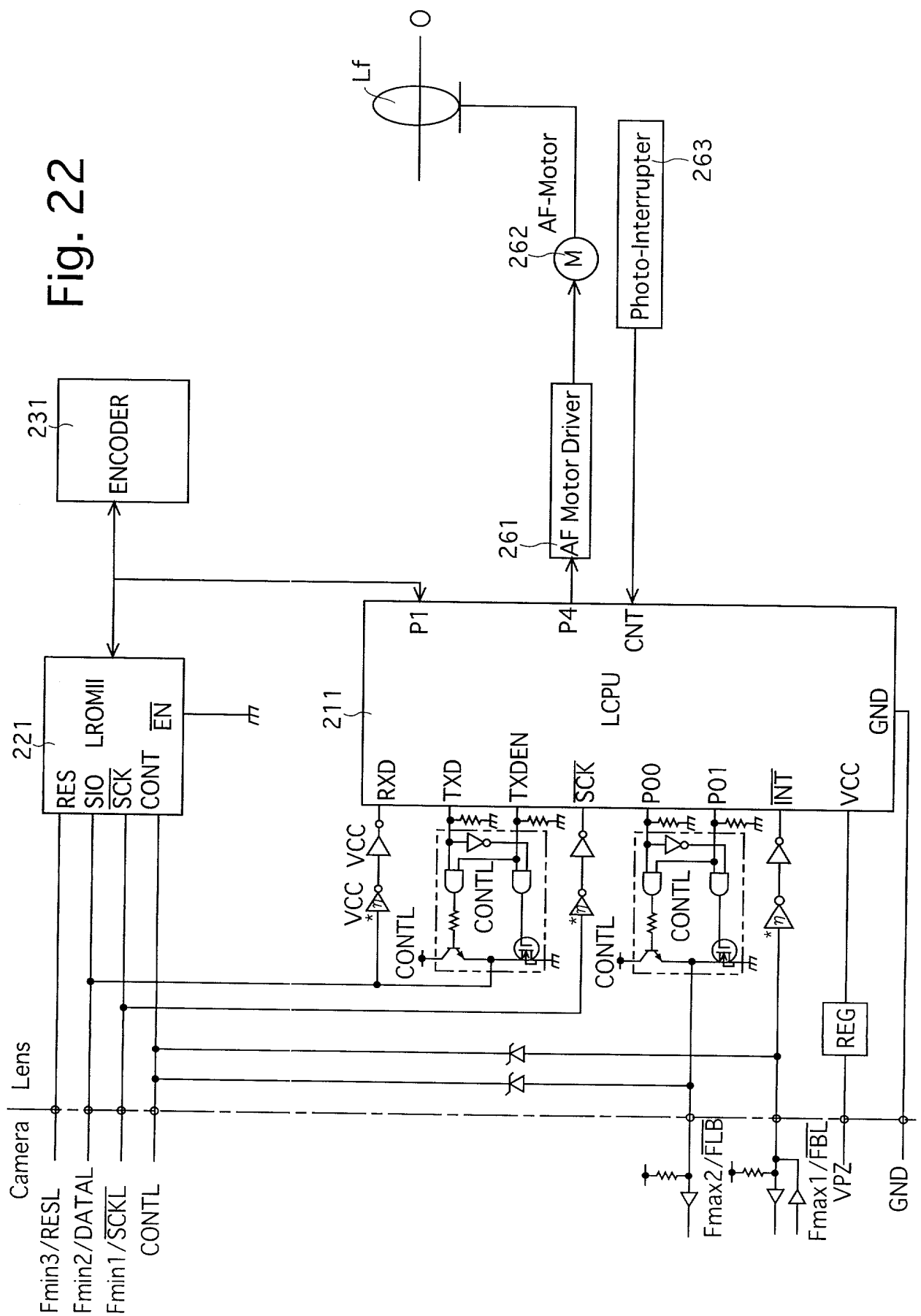


Fig. 23

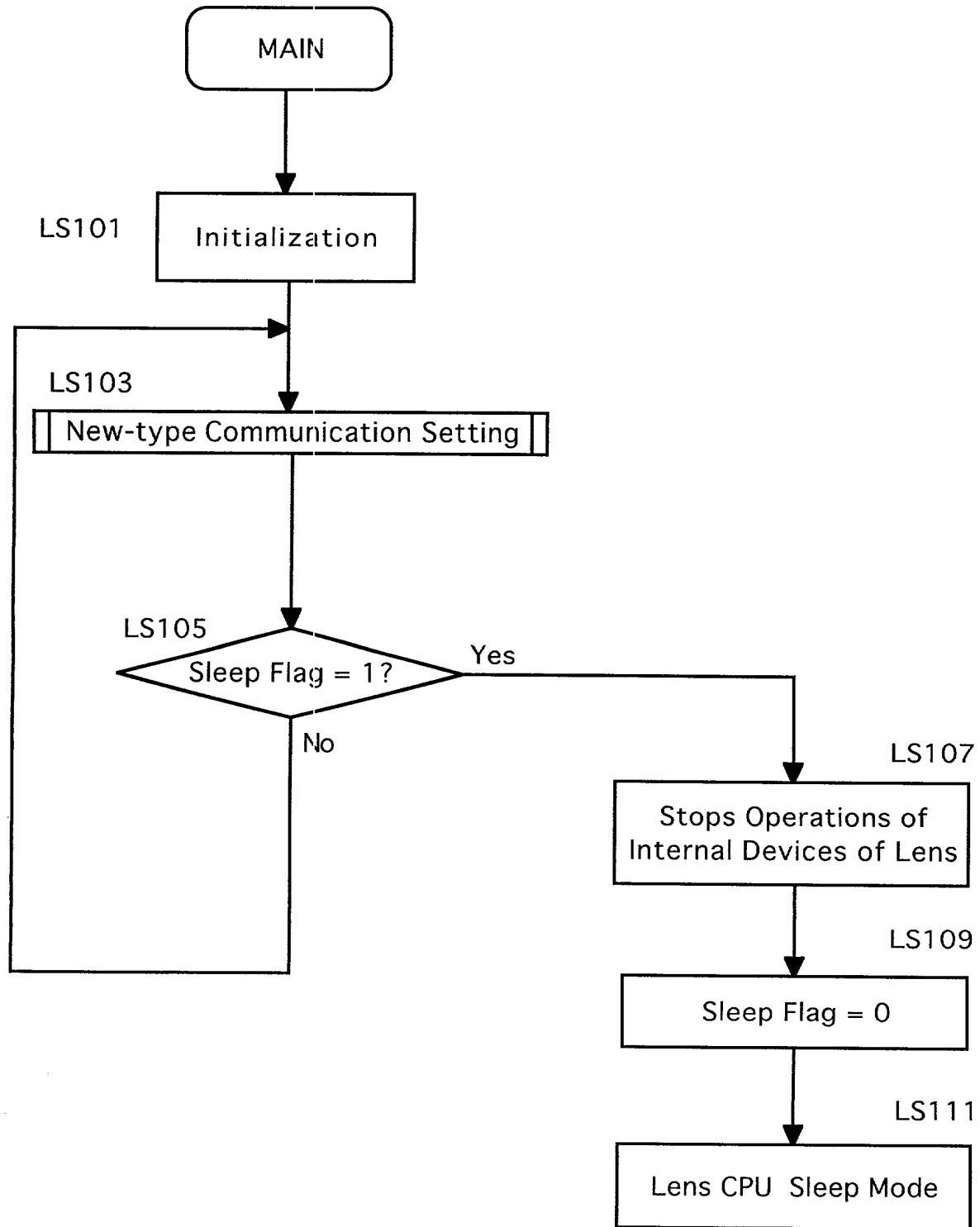
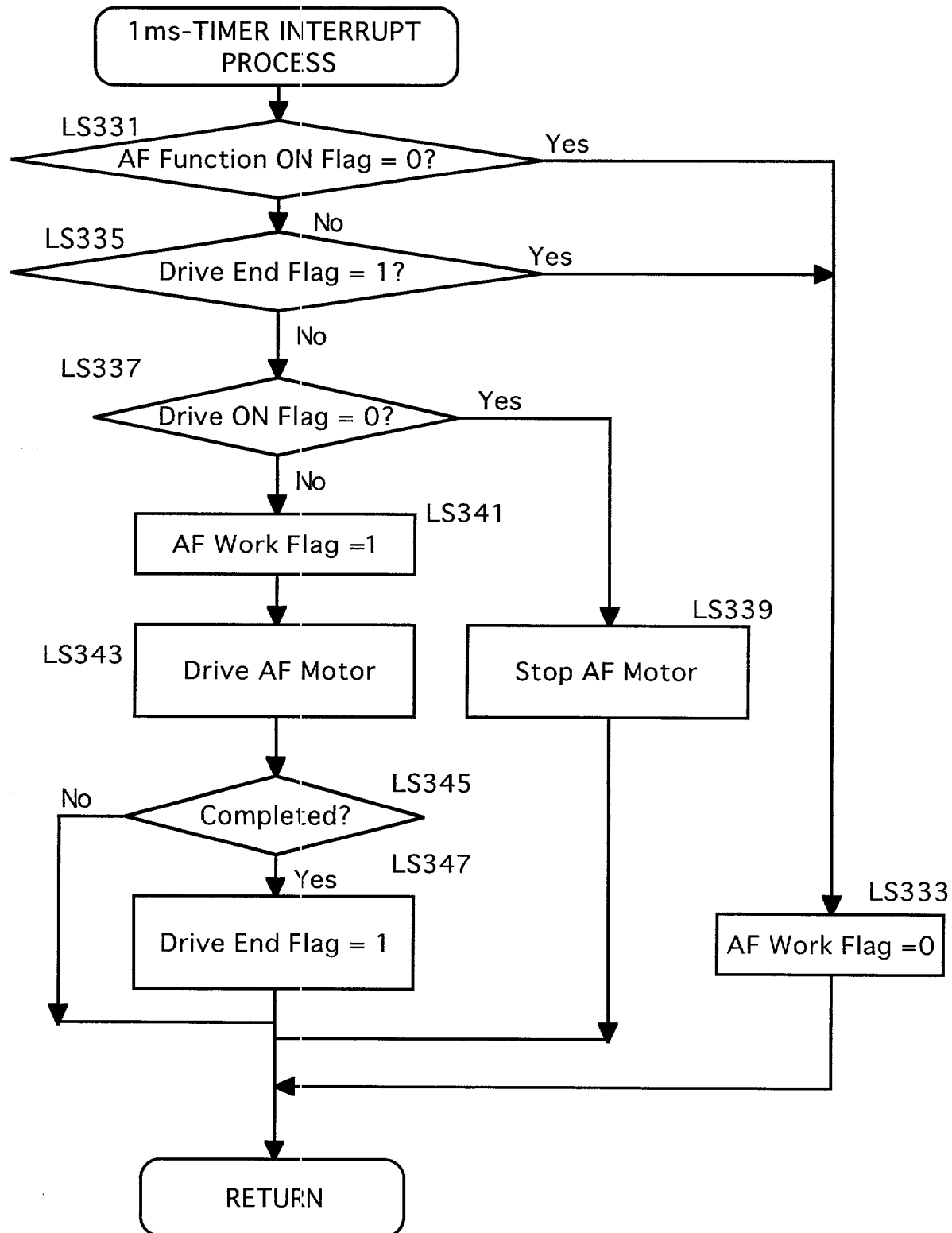


Fig. 24



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# INVERSE INT INTERRUPT PROCESS

Fig. 25

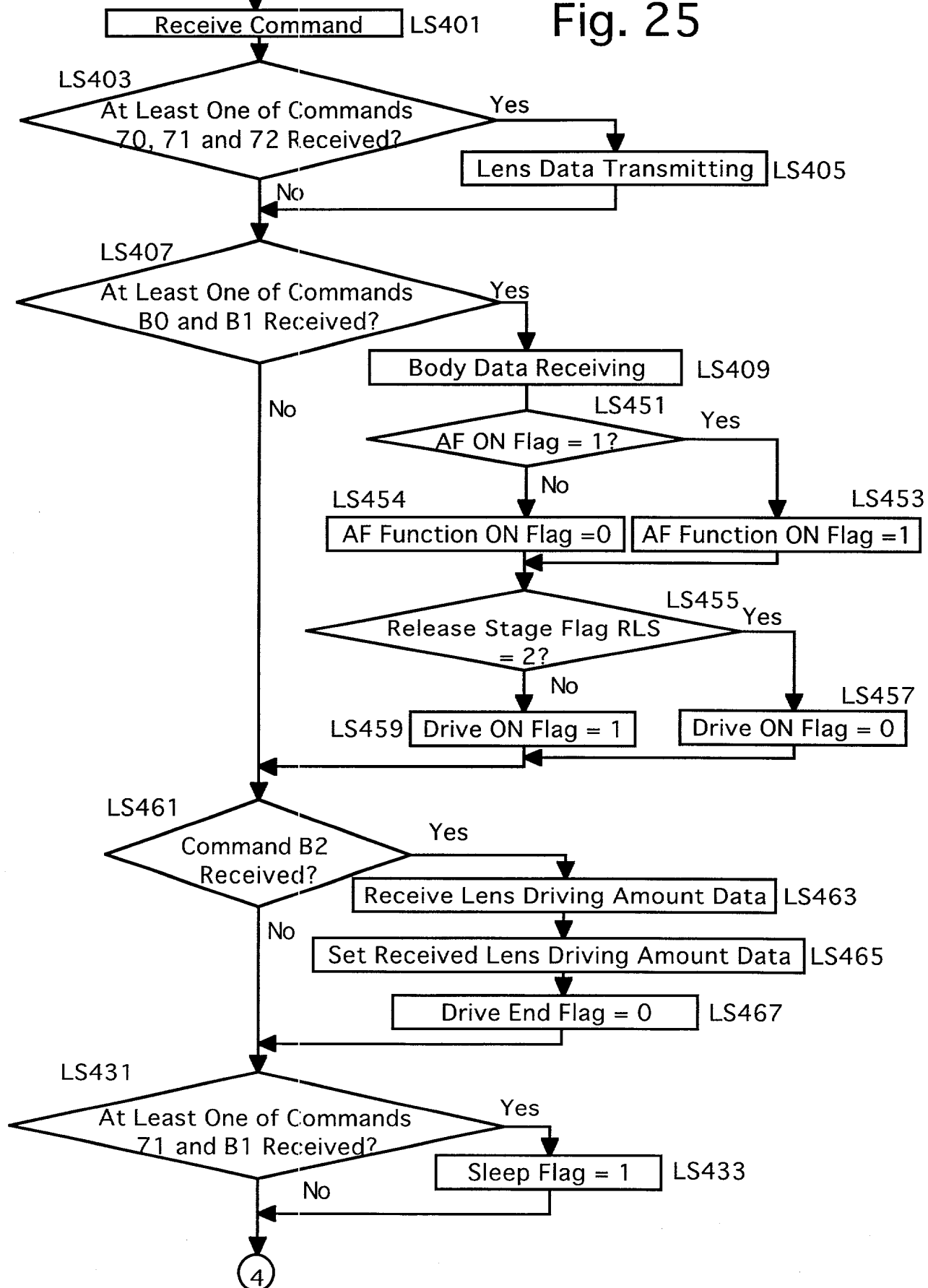


Fig. 26

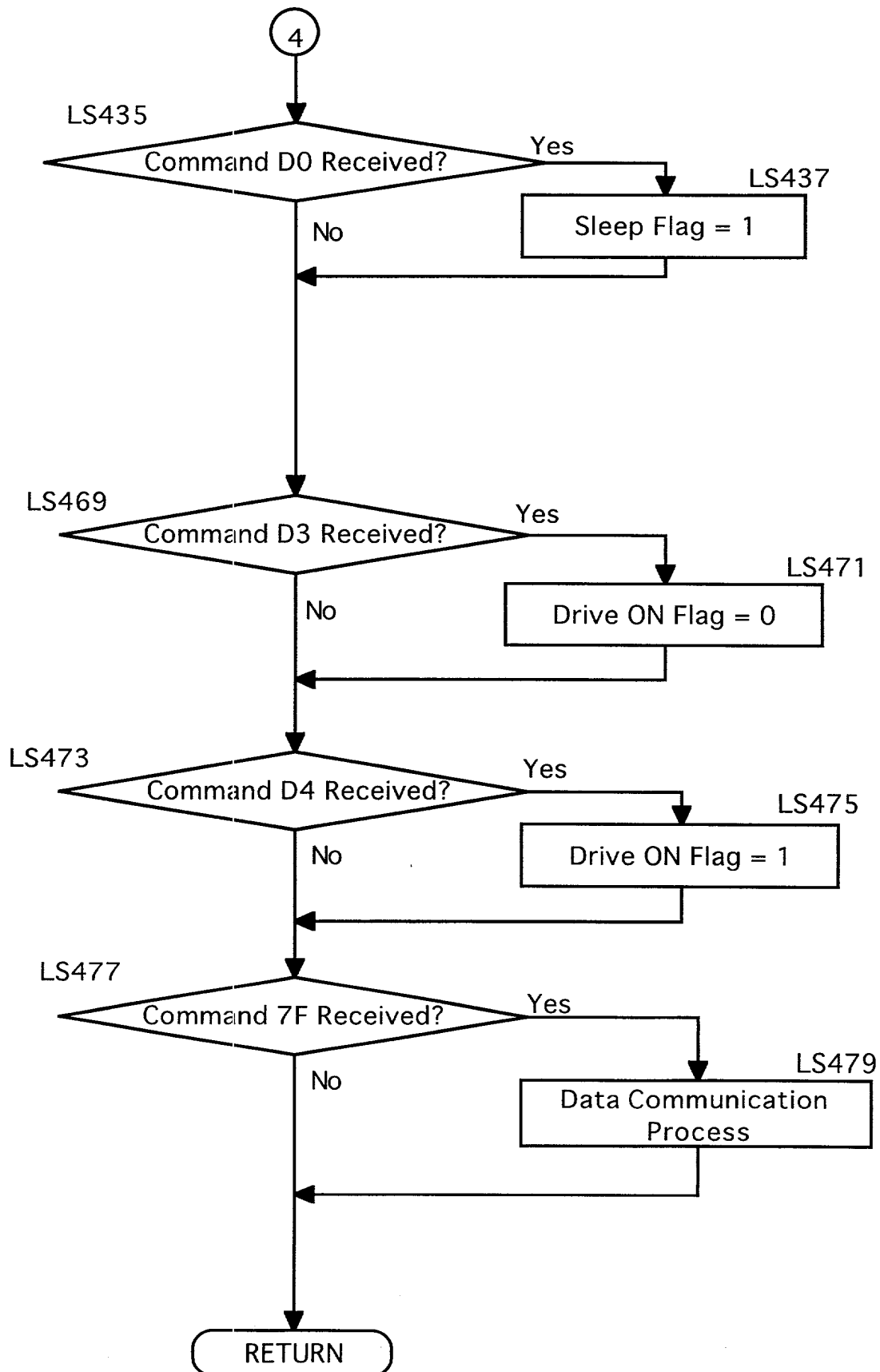


Fig.27

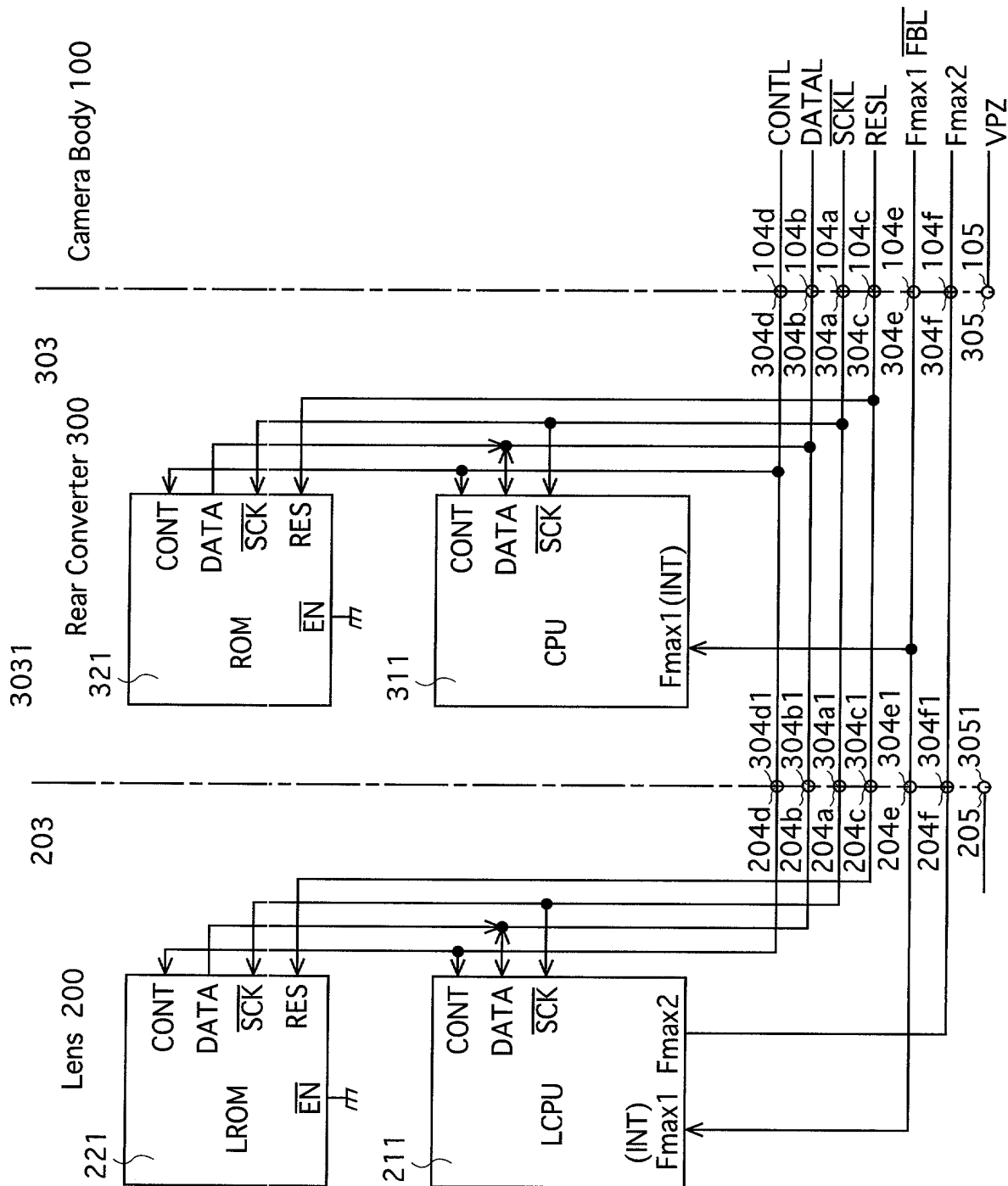
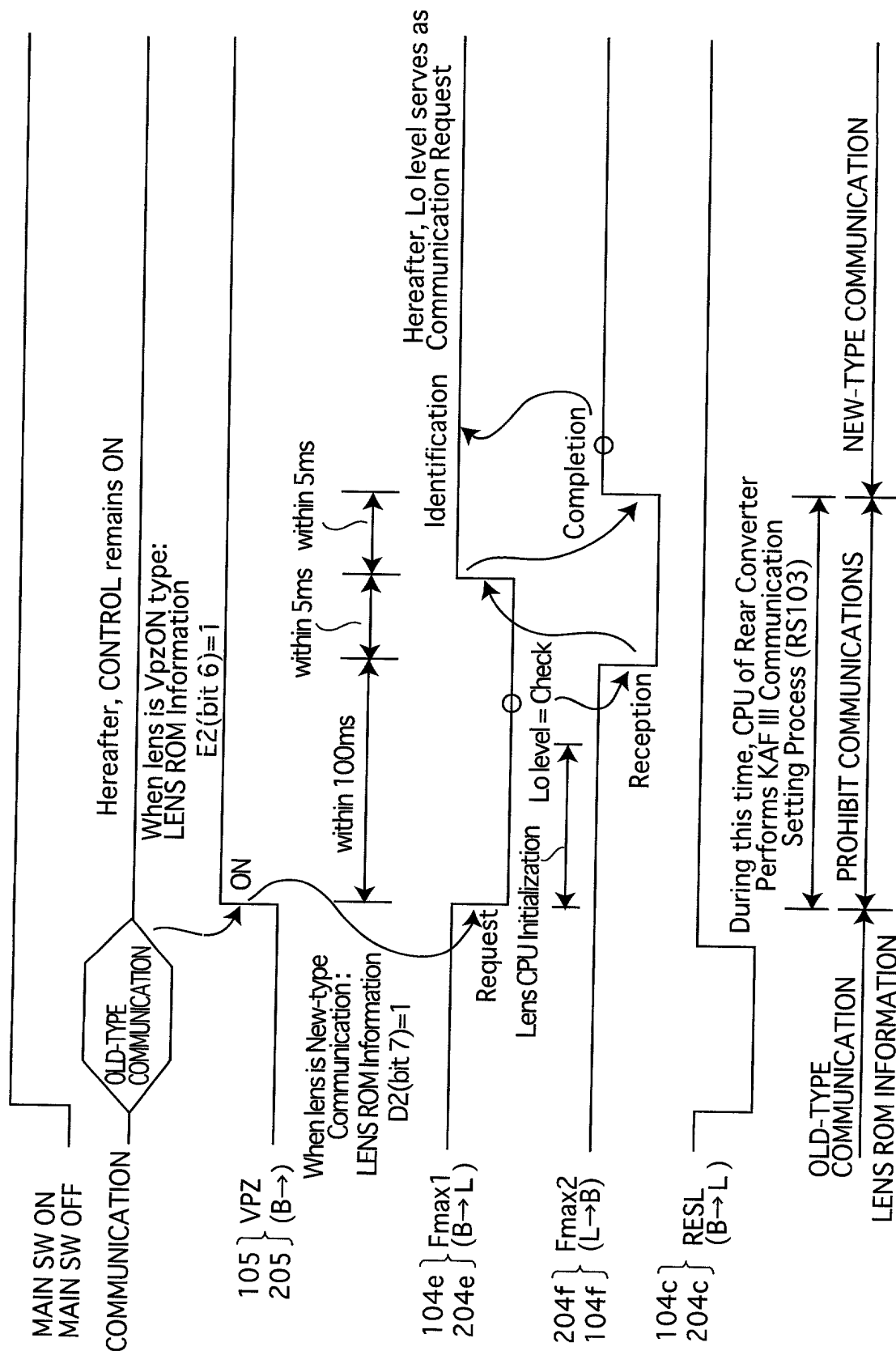
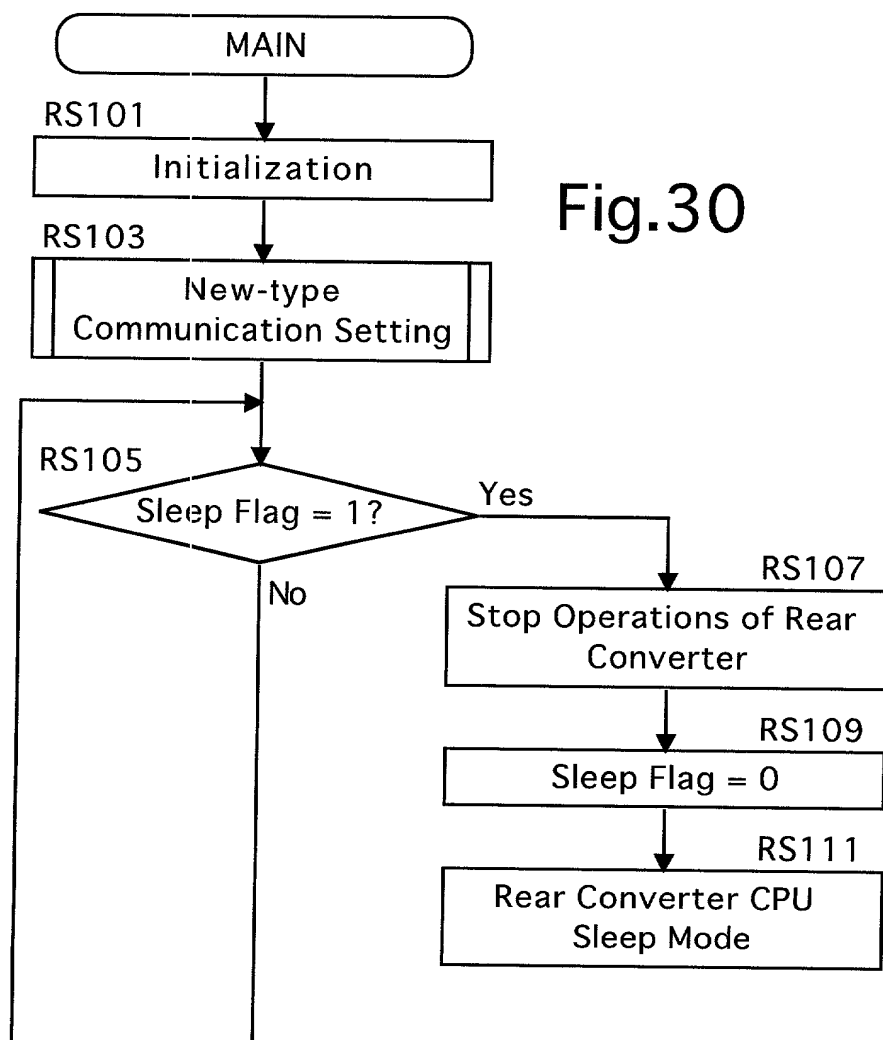


Fig. 28







[illegible]



INVERSE INT INTERRUPT PROCESS

Fig.32

